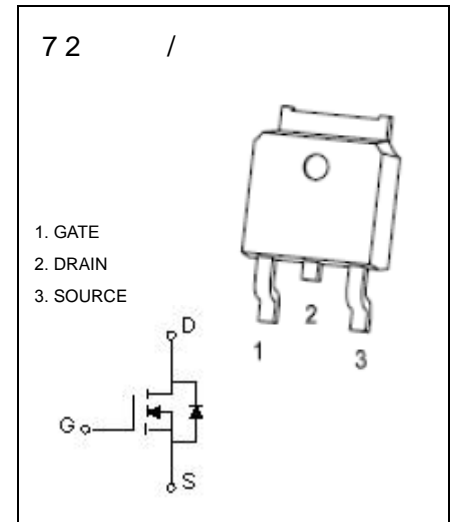


72 / 30D VWLF (QFDSVXODWH

& -8 1 N-Channel Power MOSFET

* HQHUDO 'HVFULSWLRQ

The high voltage MOSFET uses an advanced termination scheme to provide enhanced voltage-blocking capability without degrading performance over time. In addition, this advanced MOSFET is designed to withstand high energy in avalanche and commutation modes. The new energy efficient design also offers a drain-to-source diode with a fast recovery time. Designed for high voltage, high speed switching applications in power suppliers, converters and PWM motor controls, these devices are particularly well suited for bridge circuits where diode speed and commutating safe operating areas are critical and offer additional and safety margin against unexpected voltage transients.



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- z Robust High Voltage Termination
- z Avalanche Energy Specified
- z Source-to-Drain Diode Recovery Time Comparable to a Discrete Fast Recovery Diode
- z Diode is Characterized for Use in Bridge Circuits
- z I_{DSS} and $V_{DS(on)}$ Specified at Elevated Temperature

0D [LPXP UD W₆L Q/J VX Q70HV V RWKHUZLVH QRWHG

3 D U D P W H	\ P E R O	9 D O X H 6	8 Q W
Drain-Source voltage	V_{DS}	600	V
Gate-Source Voltage	V_{GS}	±20	
Continuous Drain Current	I_D	2	A
Pulsed Drain Current	I_{DM}	8	
Power Dissipation	P_D	1.25	W
Single Pulsed Avalanche Energy*	E_{AS}	128	mJ
Thermal Resistance from Junction to Ambient	R_{JA}	100	/ /W
Junction Temperature	T_J	150	/
Storage Temperature	T_{stg}	-50 ~+150	

* E_{AS} condition: $T_j=25 / , V_{DD}=50V, L=64mH, I_{AS}=2A, R_G=25 \checkmark$

(OHFWULFDO FKDJDFWHLVWLFRWKHUZLVH QRWHG

3DUDPHWHU	6\PERO	7HVW & RQG	WLRO	7\ S	0D[8QLW
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	600			V
Gate-Threshold Voltage (note1)	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	2.0		4.0	
Gate-Body Leakage Current (note1)	I_{GSS}	$V_{DS} = 0V, V_{GS} = \pm 20V$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 600V, V_{GS} = 0V$			25	μA
Drain-Source On-State Resistance (note1)	$R_{DS(on)}$	$V_{GS} = 10V, I_D = 1A$			4.4	Ω
Forward Transconductance (note1)	g_{fs}	$V_{DS} = 50V, I_D = 1A$	1			S
Input Capacitance (note2)	C_{iss}	$V_{DS} = 25V, V_{GS} = 0V,$ $f = 1MHz$		435		pF
Output Capacitance (note2)	C_{oss}			56		
Reverse Transfer Capacitance (note2)	C_{rss}			9.2		
Turn-On Delay Time (note2)	$t_{d(on)}$	$V_{DD} = 300V, I_D = 2A,$ $V_{GS} = 10V, R_G = 18\Omega$		12		ns
Rise Time (note2)	t_r			21		
Turn-Off Delay Time (note2)	$t_{d(off)}$			30		
Fall Time (note2)	t_f			24		
Forward on Voltage(note1)	V_{SD}	$V_{GS} = 0V, I_S = 2A$			1.6	V

1 R W H V

1. Pulse Test : Pulse Width $\leq 300\mu s$, duty cycle $\leq 2\%$.
2. These parameters have no way to verify.

